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DESIGNATED/ELECTED OFFICE (DO/EO/US)
CONCERNING A FILING UNDER 35 U.S.C. 371

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PRIORITY DATE CLAIMED
Yes

TITLE OF INVENTION

ELECTROSTATIC CHUCK AND ITS MANUFACTURING METHOD

APPLICANT(S) FOR DO/EO/US

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Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☐ This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (24) indicated below.
4. ☒ The US has been elected by the expiration of 19 months from the priority date (Article 31).
5. ☒ A copy of the International Application as filed (35 U.S.C. 371 (c) (2))
 - a. ☐ is attached hereto (required only if not communicated by the International Bureau).
 - b. ☒ has been communicated by the International Bureau.
 - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☐ An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)).
 - a. ☐ is attached hereto.
 - b. ☐ has been previously submitted under 35 U.S.C. 154(d)(4).
7. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3))
 - a. ☐ are attached hereto (required only if not communicated by the International Bureau).
 - b. ☐ have been communicated by the International Bureau.
 - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
 - d. ☒ have not been made and will not be made.
8. ☐ An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☐ An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4))
10. ☐ An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).
11. ☒ A copy of the International Preliminary Examination Report (PCT/IPEA/409).
12. ☒ A copy of the International Search Report (PCT/ISA/210).

Items 13 to 20 below concern document(s) or information included:

13. ☒ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
14. ☐ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
15. ☐ A **FIRST** preliminary amendment
16. ☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
17. ☐ A substitute specification.
18. ☐ A change of power of attorney and/or address letter.
19. ☐ A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825.
20. ☒ A second copy of the published international application under 35 U.S.C. 154(d)(4).
21. ☐ A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).
22. ☒ Certificate of Mailing by Express Mail
23. ☐ Other items or information:

U.S. APPLICATION NO (IF KNOWN) 10/030592 INTERNATIONAL APPLICATION NO PCT/JP99/03701				ATTORNEY'S DOCKET NUMBER LAM2P322	
24. The following fees are submitted: BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) : <input type="checkbox"/> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO \$1040.00 <input checked="" type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO \$890.00 <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO \$740.00 <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) \$710.00 <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) \$100.00 <div style="text-align: right;">ENTER APPROPRIATE BASIC FEE AMOUNT =</div>				CALCULATIONS PTO USE ONLY <div style="border: 1px solid black; height: 100px; width: 100%;"></div>	
Surcharge of \$130.00 for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input checked="" type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492 (e)).				\$890.00 \$130.00	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total claims	8 - 20 =	0	x \$18.00	\$0.00	
Independent claims	2 - 3 =	0	x \$84.00	\$0.00	
Multiple Dependent Claims (check if applicable). <input type="checkbox"/>				\$0.00	
TOTAL OF ABOVE CALCULATIONS =				\$1,020.00	
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27). The fees indicated above are reduced by 1/2.				\$0.00	
SUBTOTAL =				\$1,020.00	
Processing fee of \$130.00 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492 (f)).				\$0.00	
TOTAL NATIONAL FEE =				\$1,020.00	
Fee for recording the enclosed assignment (37 CFR 1.21(h)) The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable). <input type="checkbox"/>				\$0.00	
TOTAL FEES ENCLOSED =				\$1,020.00	
				Amount to be:	\$
				refunded	\$
				charged	\$
a. <input checked="" type="checkbox"/> A check in the amount of \$1,020.00 to cover the above fees is enclosed. b. <input type="checkbox"/> Please charge my Deposit Account No. _____ in the amount of _____ to cover the above fees. A duplicate copy of this sheet is enclosed. c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-0805 LAM322 A duplicate copy of this sheet is enclosed. d. <input type="checkbox"/> Fees are to be charged to a credit card. WARNING: Information on this form may become public Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.					
NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.					
SEND ALL CORRESPONDENCE TO:					
PENILLA, Albert S. MARTINE & PENILLA, LLP 710 Lakeway Drive, Suite 170 Sunnyvale, CA 94085				<div style="text-align: center;">  SIGNATURE Albert S. Penilla NAME 39,487 REGISTRATION NUMBER January 7, 2002 DATE </div>	

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of

Nakajima et al.

Application No: Unassigned

Filing Date: January 7, 2002

For: ELECTROSTATIC CHUCK AND ITS
MANUFACTURING METHOD

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) Examiner: Unassigned

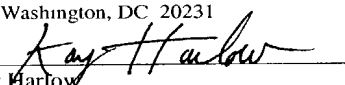
) Art Unit: Unassigned

) Docket No: LAM2P322

) January 7, 2002

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this paper and the documents and/or fees referred to as attached herein are being deposited with the United States Postal Service on January 7, 2002 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number **EL896843413US**, addressed to the Designated/Elected Office (DO/EO/US), Washington, DC 20231


Kay Harlow

TRANSMITTAL OF DOCUMENTS FOR FILING UNDER 35 U.S.C. § 371

Designated/Elected Office (DO/EO/US)
Washington, D.C. 20231

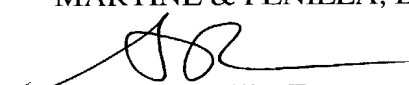
Sir:

Applicant is hereby transmitting the following documents for the above-identified patent application:

1. Transmittal Letter to the United States Designated/Elected Office for filing under 35 U.S.C. § 371;
2. Information Disclosure Statement and Form 1449;
3. One cited reference (EP 0 805 487 A2);
4. Copy of the International Search Report;
5. Copy of the International Preliminary Examination Report;
6. Copy of International Application No. WO 01/04945 A1 (PCT/JP99/03701); and
7. A check in the amount of \$1,020.00, to cover the above costs.

Applicant believes that no further fees are due in connection with the filing of the above-named documents. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 50-0805 (Order No. LAM2P322).

Respectfully submitted,
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DESCRIPTION

ELECTROSTATIC CHUCK AND ITS MANUFACTURING METHOD

Field of the Invention

The present invention relates to an electrostatic chuck for use in an etching apparatus for manufacturing semiconductors and its manufacturing method.

Background Art

In a semiconductor manufacturing process, etching processes are repeatedly performed together with insulating film formation, diffusion processes and photolithographic processes. There are two types of etching process; wet etching and dry etching. The dry etching is implemented using a plasma etching apparatus as shown in Fig. 4, for example, with a semiconductor wafer W held on a chuck 12 in a processing chamber 11 of the etching apparatus, a reactive gas is introduced from an inlet 13 into the above-described chamber 11 while high-frequency electric power 15 is applied between the chuck 12 which serves as a lower electrode and an upper electrode 14 so as to generate a plasma in the processing chamber 11, by which chemical reaction with radical in the plasma and accelerated ions etch the semiconductor wafer W, more particularly the semiconductor wafer W itself or an insulating film (not shown) thereon.

In this dry etching, the semiconductor wafer W is held on chuck 12 as described above. Recently, for the chuck 12,

an electrostatic chuck is specified due to its excellent characteristics proved in vacuum plasma processors.

The electrostatic chuck attracts material to be attracted by generating electrostatic attraction between the material to be attracted and the chuck. The electrostatic attraction is made of two types of forces; Coulomb force and Johnsen-Rahbek force.

There are two types of the electrostatic chuck; one is a unipolar type in which only an anode 22 is formed in dielectric material 21 and a cathode is made of the apparatus and a plasma electric potential as shown in Fig. 5, and the other is a bipolar type in which both an anode 32 and a cathode 33 are formed in dielectric material 31 as shown in Fig. 6.

Referring to Fig. 7, there is shown a cross-sectional view illustrating an example of a conventional practically-used electrostatic chuck. This electrostatic chuck comprises an anodized aluminum film 42 which is 50 to 60 μm in thickness as dielectric material on a surface of a disc-shaped aluminum electrode 41. The anodized aluminum film 42 is the material on which the semiconductor wafer W is placed. On a surface of the aluminum electrode 41 covered by the anodized aluminum film 42, is formed a cooling gas channel 43 extending in a circumferential direction of the electrode. To this cooling gas channel 43, a helium gas is fed as cooling gas from gas feed orifices (not shown) penetrating the anodized aluminum film 42 and the aluminum electrode 41.

The helium gas fed to the channel 43 flows into the channel 43, fills it, and then diffuses along the entire interface between the anodized aluminum film 42 and the semiconductor wafer W through fine gaps therebetween generated by a rough surface of the anodized aluminum film 42, thereby cooling the semiconductor wafer W. In the dry etching with the apparatus shown in Fig. 4, a temperature of the semiconductor wafer W significantly affects the etching characteristics. By cooling the semiconductor wafer W up to 30 °C to 60 °C with the above-described helium gas used as a heat conductive means, the etching characteristics, especially uniformity, are improved.

In the conventional electrostatic chuck as set forth in the above, however, there is a disadvantage that reaction products which have been floating or otherwise dispensed in the chamber 11 shown in Fig. 4 adhere to a surface of the anodized aluminum film 42, after the semiconductor wafer W is removed from the chuck after the etching process, thus weakening the electrostatic attraction. In addition, it has a problem that the reaction products adhering to the surface of the anodized aluminum film 42 increases gaps between the anodized aluminum film 42 and the semiconductor wafer W, causing leakage of the helium gas from an outer peripheral edge of the aluminum electrode 41, by which the semiconductor wafer W is insufficiently cooled by the helium gas and therefore the etching characteristics deteriorate. Furthermore, the anodized aluminum film 42 is deteriorated

by the reactive gas or ions which have passed through end portions of the semiconductor wafer W during etching before reaching there, thus also weakening the electrostatic attraction.

Accordingly, there has been devised an electrostatic chuck for which a ceramic layer is used for the dielectric material as shown in Fig. 8. This electrostatic chuck has a ceramic layer 53 bonded onto a disc-shaped metal base plate 51 by means of an adhesive layer 52. A high-melting point electrode 54 is laid in the ceramic layer 53. In this arrangement, from the viewpoint of increasing the electrostatic attraction, the electrode 54 is located in the ceramic layer 53 relatively near the surface thereof. Specifically, on the assumption that the ceramic layer 53 is 1 mm in thickness, for example, the electrode 54 is located in a position spaced 0.3 mm away from the surface of the ceramic layer 53 and spaced 0.7 mm away from the bottom thereof. In the same manner as for the electrostatic chuck shown in Fig. 7, is formed a cooling gas channel 55 extending in a circumferential direction of the ceramic layer 53 on the surface thereof. This channel 55 needs a certain depth taking into consideration a flow of the helium gas. Therefore, if the above-described channel 55 is formed in the position of the electrode 54 located relatively near the surface of the ceramic layer 53, a distance between the bottom of the channel 55 and the electrode 54 becomes short, thus causing an insufficient dielectric strength of the

ceramic layer 53. Accordingly, in this example, the channel 55 is formed 1 to 2 mm within the end of the ceramic layer 53, and the electrode 54 is formed further within the channel 55.

The above-described electrostatic chuck has a problem, however; the electrode 54 having a coefficient of linear thermal expansion different from that of the ceramic layer 53 is located in the ceramic layer 53 relatively near the surface thereof, which often causes a warpage of the ceramic layer 53 if the ceramic layer 53 within the electrode 54 is formed by firing. In addition, while a leakage of the helium gas fed to the channel 55 from the end portion of the ceramic layer 53 is prevented by the sealed portion between the ceramic layer 53 and the semiconductor wafer W between the channel 55 and the end portion of the ceramic layer 53, such a short distance of the sealed portion (a sealed distance) as 1 to 2 mm sometimes causes a gas leakage.

In order to prevent the gas leakage, there is also another example that the channel 55 is formed approximately 3 to 10 mm within the end of the ceramic layer 53 as shown in Fig. 9, so as to achieve a substantial sealed distance. This example also has a problem, however; the electrode 54 is formed further within the channel 55 located within the end of the ceramic layer 53, which effectively decreases an area of the electrode 54, thus causing insufficient electrostatic attraction in the helium gas sealed portion.

Furthermore, there are many examples of conventional

electrostatic chucks in which a wafer W attracted to a chuck cannot always be readily detached, or "dechucked" therefrom after a completion of etching or other processes. In some cases, it takes a considerable time for detachment of the wafer w.

The present invention overcomes problems associated with prior art arrangements, and provides an electrostatic chuck and its manufacturing method for preventing warpage of a ceramic layer and a cooling gas leakage in addition to having enhanced electrostatic attraction and requiring only a short time for detachment.

Summary Disclosure of the Invention

The present invention provides an electrostatic chuck in which a disc-shaped ceramic layer having a predetermined thickness is bonded to a metal base plate by means of an adhesive layer, a planar electrode is located in the ceramic layer in the middle thereof in the thickness direction, a cooling gas channel is formed on a surface of the ceramic layer within the outer edge of the electrode and over the electrode, and the electrode extends beyond the cooling gas channel.

An alternative aspect of the invention provides a method of manufacturing an electrostatic chuck comprising the steps of preparing a first disc-shaped ceramic material compact having a thickness which is approximately one-half of a completed ceramic layer, forming an electrode on a

surface of the first ceramic material compact, preparing a second disc-shaped ceramic material compact having a thickness which is approximately one-half of the completed ceramic layer and including a cooling gas channel on its surface within the electrode, placing the second ceramic material compact on the first ceramic material compact to form a laminate and then firing the entire laminate so as to complete the ceramic layer, and bonding the completed ceramic layer onto a metal base plate by means of an adhesive layer.

Brief Description of the Drawings

Fig. 1 is a cross-sectional view of an embodiment of an electrostatic chuck according to the present invention;

Fig. 2 is a top plan view of an electrode of the electrostatic chuck illustrated in Fig. 1;

Fig. 3 is a top plan view of a ceramic layer of the electrostatic chuck illustrated in Fig. 1;

Fig. 4 is a schematic view illustrating a dry etching apparatus;

Fig. 5 is a schematic view illustrating a unipolar-type electrostatic chuck;

Fig. 6 is a schematic view illustrating a bipolar-type electrostatic chuck;

Fig. 7 is a cross-sectional view illustrating a conventional first electrostatic chuck;

Fig. 8 is a cross-sectional view illustrating a

conventional second electrostatic chuck; and

Fig. 9 is a cross-sectional view illustrating a conventional third electrostatic chuck;

Detailed Description of the Invention

A description will be given of an electrostatic chuck and its manufacturing method according to the present invention, by referring to the accompanying drawings. The electrostatic chuck of the present invention is used for a dry etching apparatus. A general configuration of the dry etching apparatus is shown in Fig. 4. Accordingly, a description of the general configuration of the dry etching apparatus is omitted here, and only the electrostatic chuck is described in detail below.

Referring to Fig. 1, there is shown a cross-sectional view of an electrostatic chuck of an embodiment according to the present invention. As shown in this diagram, in the electrostatic chuck of this embodiment, a disc-shaped ceramic layer 80 having a predetermined thickness is bonded onto a disc-shaped metal base plate 60 by means of an adhesive layer 70. In this ceramic layer 80, a planar electrode 90 made of tungsten, for example, is laid in parallel to a surface of the ceramic layer 80. At this point, the electrode 90 is formed in the ceramic layer 80 at a depth of one half of the thickness thereof, in other words, in the middle of the ceramic layer 80 in the thickness direction.

A top plan view of this electrode 90 is shown in Fig. 2. The electrode 90 comprises a first electrode 91 and a second electrode 92. Therefore, the electrostatic chuck of this embodiment is of a bipolar type as shown in Fig. 6. The first electrode 91 comprises a disc portion 91a arranged in the center of the ceramic layer 80, a linear portion 91b linearly extending from a part of the disc portion 91a toward an outer peripheral edge of the ceramic layer 80. The first electrode 91 further comprises a plurality of "C-shaped" ring portions 91c at predetermined intervals having different diameters and extending in "C" shapes on both sides of the linear portion 91b so as to surround the disc portion 91a. The second electrode 92 comprises a linear portion 92b positioned opposite to the linear portion 91b of the first electrode 91 beyond the disc portion 91a of the first electrode 91 and arranged complementary to the plurality of the C-shaped ring portions 91c of the first electrode 91, a plurality of "C-shaped" ring portions 92c at predetermined intervals having different diameters and extending in "C" shapes on both sides of the linear portion 92b, and, being engaged with the plurality of the C-shaped ring portions 91c of the first electrode 91. Furthermore, the electrode 91 comprises a circular ring portion 92d connected to an outer edge of the linear portion 92b so as to form the outermost peripheral portion of the electrode.

Returning to Fig. 1, a cooling gas channel 81 is formed on a surface of the ceramic layer 80. The cooling gas

channel 81 is provided in a ring shape along the outer peripheral edge of the ceramic layer 80 as shown in a top plan view in Fig. 3. In addition, the channel 81 is formed within the outer peripheral edge of the electrode 90 and over the electrode 90 as shown in Fig. 1. Accordingly, the electrode 90 passes under the bottom of the channel 81 and extends beyond the channel 81 and up to the vicinity of the outer peripheral side of the ceramic layer 80.

At a bottom of the channel 81, there are gas feed orifices 82 in a plurality of positions in a circumferential direction as shown in Fig. 3. Additionally on a surface of the ceramic layer 80 in the center side, gas feed orifices 83 are arranged in a plurality of positions on a single circumference. These gas feed orifices 82 and 83 extend through the ceramic layer 80, the adhesive layer 70, and the base plate 60 up to the bottom thereof. A cooling gas such as a helium gas, for example, is fed from the bottom of the base plate 60. The helium gas fed to the gas feed orifices 82 and 83 spouts from the bottom of the channel 81 and from the surface of the ceramic layer 80 in the center and diffuses from the outer peripheral portion of the ceramic layer 80 and from the center thereof on the entire interface between the ceramic layer 80 and the semiconductor wafer W, as shown in Fig. 1, so as to cool the semiconductor wafer W.

According to the electrostatic chuck of this embodiment as described above, the electrode 90 comprising a high-melting point metal having a coefficient of linear

thermal expansion different from that of the ceramic layer 80 is disposed/located in the ceramic layer 80 in the middle thereof in its thickness direction. Therefore, even if the ceramic layer 80 is formed in the electrode 90 with firing, the ceramic layer 80 is not warped so as to obtain a flat ceramic layer 80. In addition, improvement of the flatness of the ceramic layer 80 leads to improvement of etching characteristics(for example, uniformity) of the semiconductor wafer W as well as increasing the attraction of the semiconductor wafer W.

Furthermore, the cooling gas channel 81 is formed within the outer peripheral edge of the electrode 90 and over the electrode 90. Thus, the electrode 90 extends beyond the channel 81, in other words, up to the gas-sealed portion. Therefore, electrostatic attraction is also applied to the sealed portion by means of the electrode 90 as indicated by arrows, thus preventing a gas leakage from the sealed portion with the electrostatic attraction. The prevention of the gas leakage leads to improved cooling condition of the semiconductor wafer W, improving the etching characteristics, such as uniformity center-to-edge as measured on the wafer.

Furthermore, by positioning the electrode 90 in the middle (in a deep position) of the ceramic layer 80 in its thickness direction, the channel 81 can be formed sufficiently deep even if it is located over the electrode 90, by which the helium gas smoothly flows so as to cool the semiconductor wafer W favorably. In addition, by forming the

channel 81 over the electrode 90, a long distance of the sealed portion can be secured by disposing the channel 81 within the ceramic layer 80, by which a gas leakage can be prevented more reliably and an area of the electrode does not need to be decreased due to a position of the channel 81. Also, even if the electrode 90 is located in a deep position in the ceramic layer 80, sufficient electrostatic attraction can be secured since electrostatic attraction is also applied to the sealed portion as described in the above.

Furthermore, by using a pattern shown in Fig. 2 for the first and second electrodes 91 and 92 of the electrode 90, the first and second electrodes 91 and 92 having an identical area can be uniformly formed all over the ceramic layer 80, by which uniform electrostatic attraction can be achieved on the entire ceramic layer 80. Still further, the ratio of the area of the electrode 91 to the area of the ceramic layer 80 can be increased, by which the electrostatic attraction is enhanced. Additionally, with the ESC of the present invention, detaching or "dechucking" performance of a wafer after processing is improved in comparison with an ESC made with conventional doughnut or threading pattern, where the conventional pattern causes an uneven ratio of an area of the first electrode to that of the second electrode, thus leading to a maldistribution of electric charges and causing poor dechucking performance.

Embodiment

The base plate 60 is preferably formed by using an

aluminum 6061, for example. A high radio frequency(RF) power is fed to this base plate 60 in order to generate a plasma in a chamber 11 of an apparatus shown in Fig. 4. The high radio frequency power to be applied may be in the range of 1 to 40 MHz in frequency and of 15 to 3000 W in power.

For the adhesive layer 70, a suitable, flexible, preferably organic, adhesive is used. With the ceramic layer 80 bonded to the base plate 60 by means of the flexible organic adhesive, the ceramic layer 80 is prevented from cracking caused by differential stress due to a difference of a coefficient of linear thermal expansion.

The ceramic layer 80 is formed by adding conductive additive to aluminum oxide, for example, aluminum nitride, or magnesium oxide. In this embodiment, it is formed by using a material of Al_2O_3 as a predominant component, and TiO_2 and glass firing auxiliary material as additives. The additive TiO_2 is added to cause the ceramic layer 80 to be slightly conductive so as to let electric charges for generating electrostatic attraction rise from the electrode 90 up to the surface of the ceramic layer 80. A resistivity of the ceramic layer 80 is selected so that it is within a range of 10^{11} - 10^{12} Ω/cm , and preferably within a range of 1.0 to 2.0×10^{11} Ω/cm .

A diameter of the ceramic layer 80 is slightly smaller than that of the semiconductor wafer W, and a thickness of the ceramic layer 80 is approximately 1 mm taking into consideration dielectric breakdown and an energy loss of the

high-frequency power. Surface roughness of the ceramic layer 80 is $Ra \approx 0.8 \mu m$ and flatness thereof is $5 \mu m$ or lower. The outer peripheral edge on the surface of the ceramic layer 80 is chamfered by 0.2 to 0.25 mm.

The cooling gas channel 81 is formed so as to extend 5 mm within the outer peripheral edge of the ceramic layer 80 and to be 1 mm wide and 0.25 mm deep. On the bottom surface of the cooling gas channel 81, eight gas feed orifices 82 are formed at 45° intervals in a circumferential direction. On the surface of the ceramic layer 80 in the center side, four gas feed orifices 83 are formed at 90° intervals on the circumference of 0.35 mm diameter. Each gas feed orifice 82 on the bottom surface of the cooling gas channel 81 is 0.32 mm in diameter and each gas feed orifice 83 on the surface of the ceramic layer 80 in the center side is 0.35 mm in diameter. The gas feed orifices 83 on the surface of the ceramic layer 80 in the center side serve as insertion orifices of pins for lifting the semiconductor wafer W, and therefore they are formed so as to be slightly larger than the gas feed orifices 82 on the bottom surface of the channel 81.

The electrode 90 (consisting of the first and second electrodes 91 and 92) is made of tungsten which is 10-20 μm in thickness. This electrode 90 is approximately 1 mm smaller than the ceramic layer 80 in radius and it is laid in a position spaced 0.5 mm away from the surface and 0.5 mm away from the bottom of the ceramic layer 80 having a

thickness of 1 mm. In other words, the electrode 90 is positioned in the middle of the ceramic layer 80 in a thickness direction.

The disc portion 91a of the first electrode 91 shown in Fig. 2 is approximately 30 mm in diameter. The linear portion 91b of the first electrode 91 and the linear portion 92b of the second electrode 92 are approximately 6.0 mm wide. There are seven C-shaped ring portions 91c of the first electrode 91 having consecutively increasing diameters and seven C-shaped ring portions 92c of the second electrode 92 having consecutively increasing diameters. A single circular ring portion 92d of the second electrode 92 is provided on the outermost periphery. The C-shaped ring portions 91c of the first electrode 91, the C-shaped ring portions 92c of the second electrode 92, and the circular ring portion 92d have widths slightly different each other, and their average width is approximately 5.0 mm. Respective portions of the first electrode 91 are spaced 1.0 mm away from respective corresponding portions of the second electrode 92. The total area of the first electrode 91 is 128.3 cm² and that of the second electrode 92 is nearly the same size, 128.4 cm².

<Manufacturing method>

Preparing a first disc-shaped ceramic material compact having a half of a thickness of a ceramic layer 80, an electrode is formed on a surface of the first ceramic material compact in a screen process printing method. Preparing a second disc-shaped ceramic material compact

having a half of the thickness of the ceramic layer 80 and including a cooling gas channel on its surface within the electrode, the second ceramic material compact is placed on the above-described first ceramic material compact for contact bonding. Afterward, the entire material is fired to complete the ceramic layer 80. In this process, no warpage occurs since the electrode is located in the middle of the ceramic layer 80 in a thickness direction. Subsequently the ceramic layer 80 is bonded to an aluminum base plate 60 by means of an adhesive layer 70.

<Other applications>

While the invention has been described above by way of an example of an electrostatic chuck according to the present invention applied to a dry etching apparatus, the electrostatic chuck of the present invention may be also be implemented within a CVD apparatus or the like, for example a target. In addition, it can be adapted for use in a chuck for electrostatically attracting material other than semiconductor wafers, for example, ceramic substrates.

Industrial Applicability

As described in detail hereinabove, according to an electrostatic chuck of the present invention and its manufacturing method, warpage of a ceramic layer is prevented. Furthermore, it prevents a cooling gas leakage at the periphery and increases an electrostatic attraction.

CLAIMES

1. An electrostatic chuck, comprising:
 - a metal base plate;
 - a disc-shaped ceramic layer having a predetermined thickness adhesively bonded to said metal base plate;
 - a planner electrode positioned in said ceramic layer in the middle thereof in its thickness direction; and
 - a cooling gas channel is formed on a surface of said ceramic layer over said electrode and within an outer peripheral edge of said electrode.
2. The electrostatic chuck according to claim 1, wherein said planner electrode extends beyond said cooling gas channel.
3. An electrostatic chuck according to claim 1, wherein said cooling gas channel comprises a ring shape along an outer peripheral edge of said ceramic layer, said chuck further comprising gas feed orifices located in a plurality of positions at a bottom portion of said gas cooling channel and gas feed orifices located in a plurality of positions on a circumference on a surface of said ceramic layer in the center side of the chuck.
4. An electrostatic chuck according to claim 1, wherein said planner electrode comprises a first electrode and a second electrode,
 - said first electrode comprising:
 - a disc portion arranged in the center of said ceramic layer; and

a first extending portion extending from a part of the disc portion toward the outer peripheral edge of said ceramic layer;

said second electrode comprising:

a second extending portion arranged opposite to said first extending portion over said disc portion of said first electrode and arranged in lacking portions of the plurality of said first C-shaped ring portions of said first electrode; and

a circular ring portion connected to an outer edge of said second extending portion so as to form the outermost peripheral portion of said second electrode.

5. An electrostatic chuck according to claim 4, wherein said first electrode further comprises

a plurality of first C-shaped ring portions at predetermined intervals so as to have different diameters extending in C shapes in both sides of said first extending portion surrounding said disc portion.

6. An electrostatic chuck according to claim 4, wherein said second electrode further comprises

a plurality of second C-shaped ring portions at predetermined intervals so as to have different diameters extending in C shapes in both sides of said second extending portion and being engaged with the plurality of said C-shaped ring portions of said first electrode.

7. A method for manufacturing an electrostatic chuck comprising the steps of:

preparing a first disc-shaped ceramic material compact having a half of a thickness of a completed ceramic layer;

forming an electrode on a surface of said first ceramic material compact;

preparing a second disc-shaped ceramic material compact having a half of a thickness of the completed ceramic layer and having a cooling gas channel on its surface within said electrode;

placing said second ceramic material compact on said first ceramic material compact so as to form a laminate and firing the entire laminate to complete a ceramic layer; and

bonding the completed ceramic layer to a metal base plate by means of an adhesive layer.

8. Method of claim 6, wherein said adhesive layer is flexible.

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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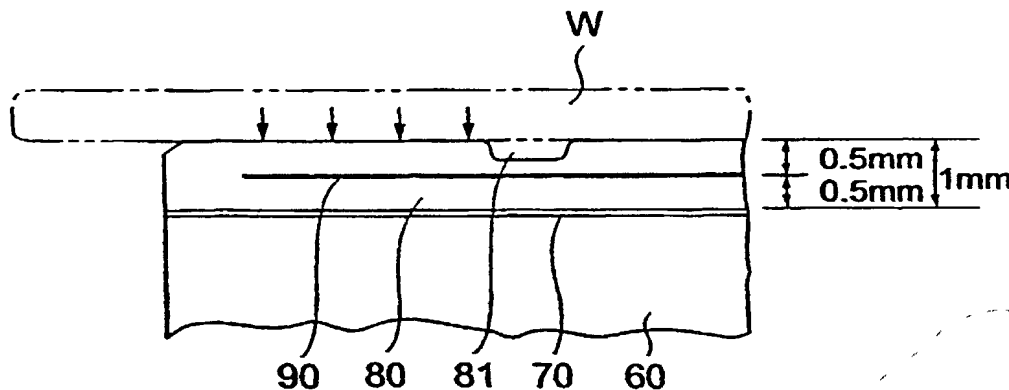
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(54) Title: ELECTROSTATIC CHUCK AND ITS MANUFACTURING METHOD



(57) Abstract: An electrostatic chuck for preventing warpage of a ceramic layer and cooling gas leakage while providing enhanced electrostatic attraction and an improved detachment performance and its manufacturing method is disclosed. The chuck comprises at least one electrode (90, 91, 92) located in the middle of the ceramic layer (80) in its thickness direction, a cooling gas channel (81) is formed on a surface of the ceramic layer within an outer edge of the electrode and above the electrode, wherein the electrode extends beyond the cooling gas channel. Preferably the electrodes are shaped in the form of two interlocked structures comprising multiple interconnected C-shaped ring portion (91c, 92c).

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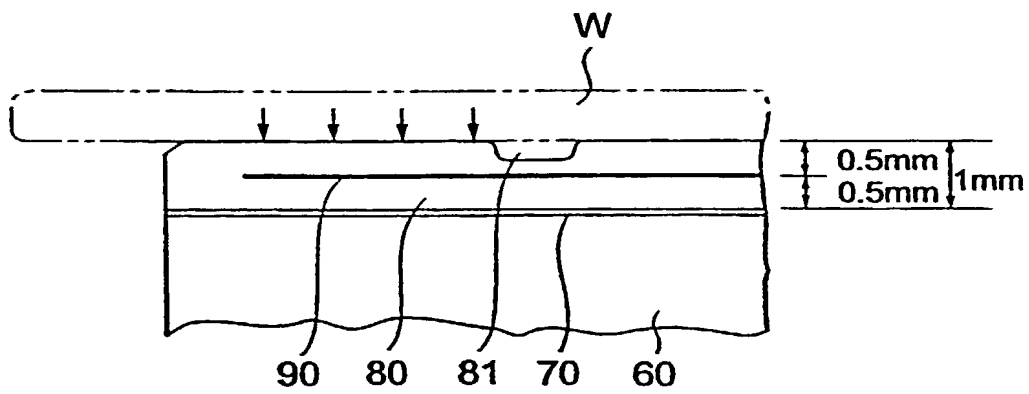


Fig. 1

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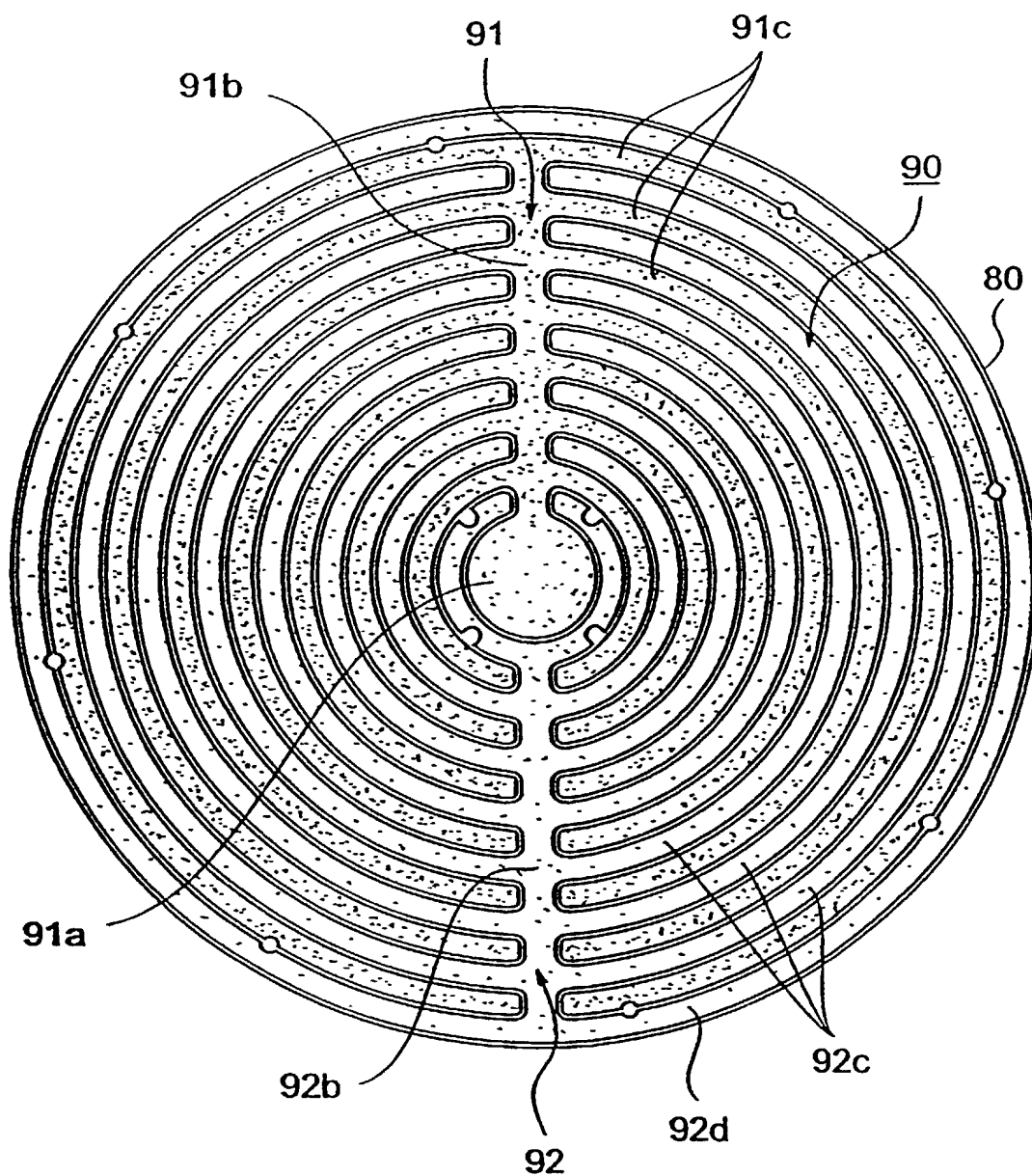


Fig. 2

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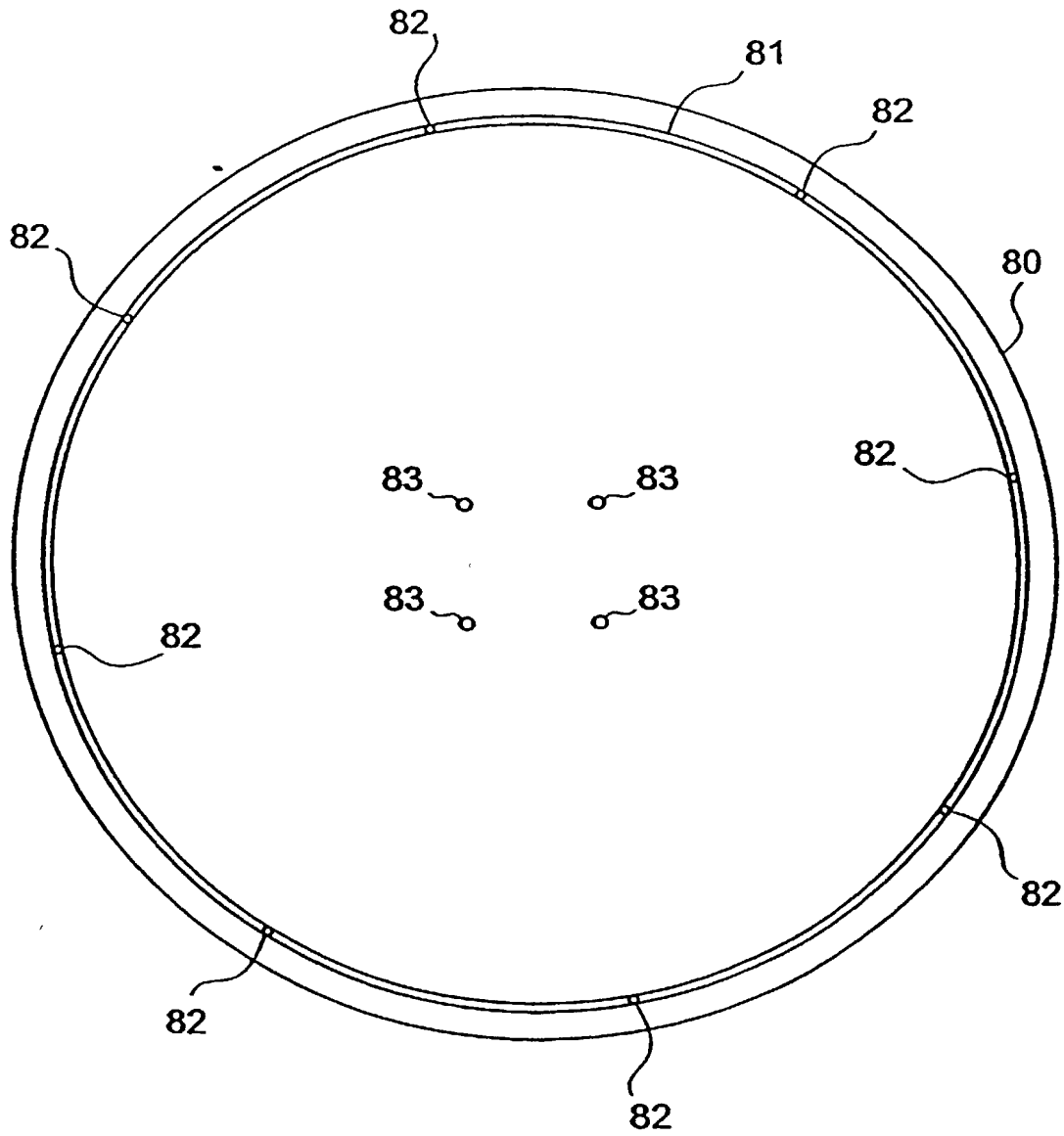


Fig. 3

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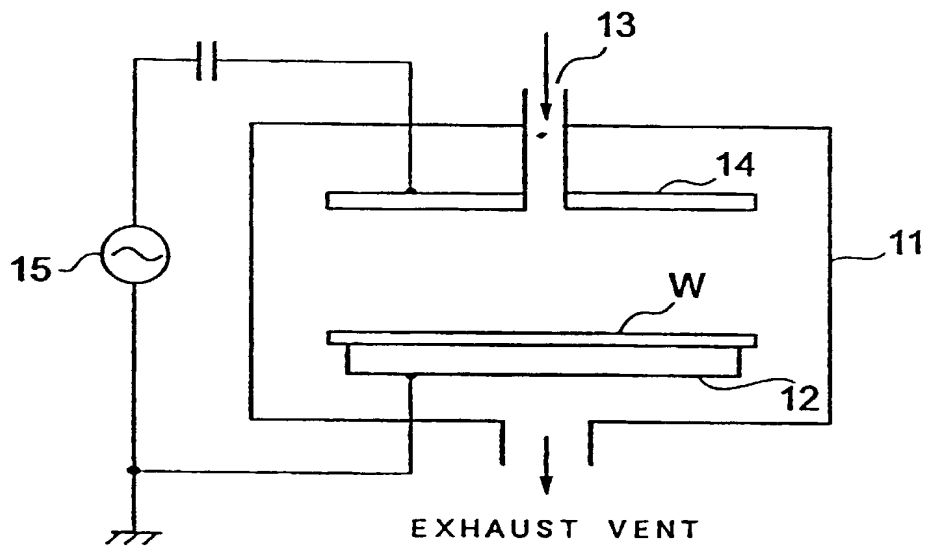


Fig. 4

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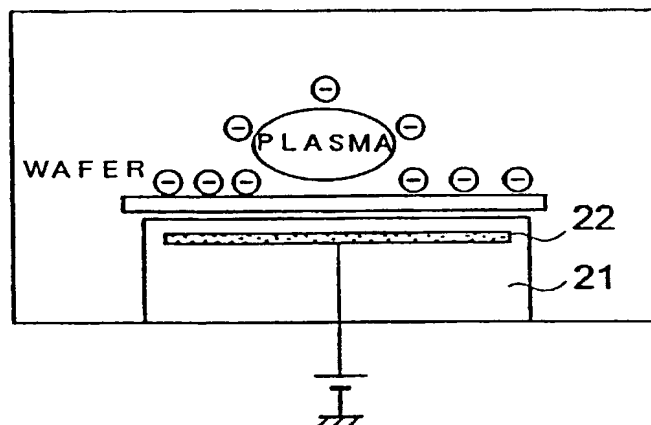


Fig. 5

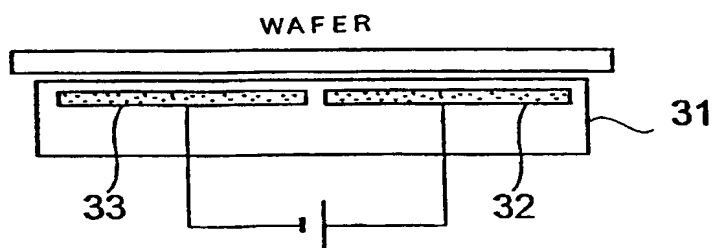


Fig. 6

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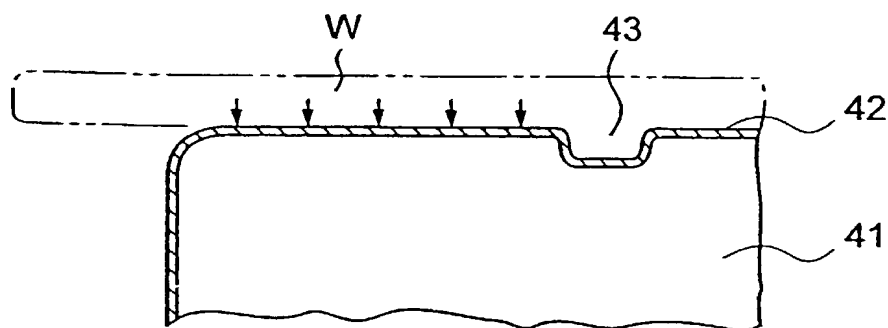


Fig. 7

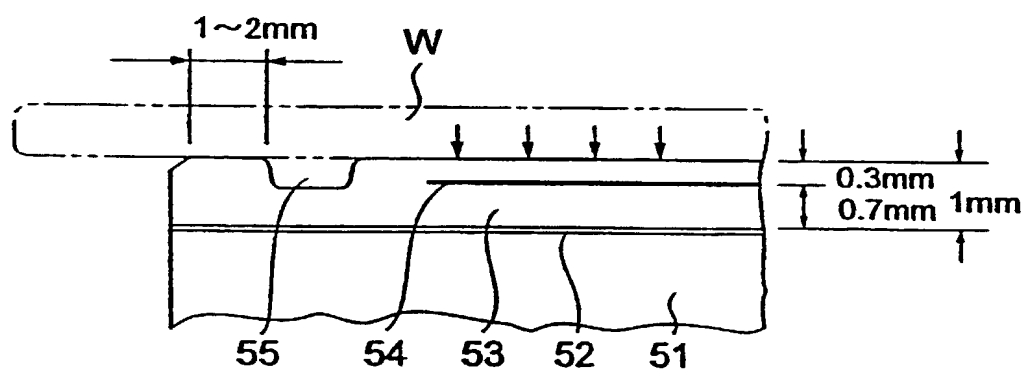


Fig. 8

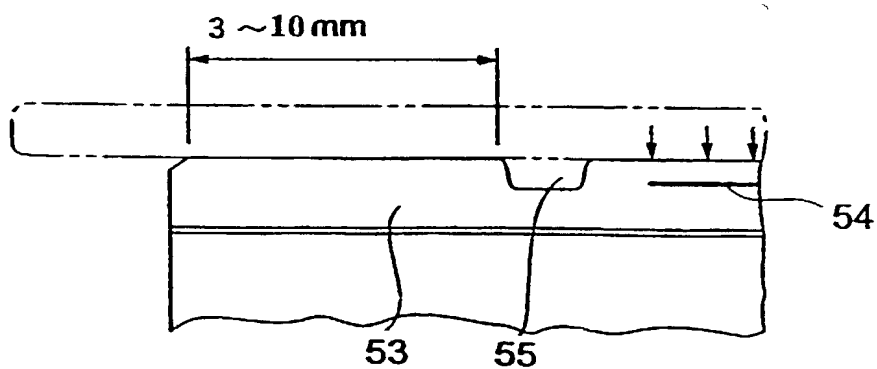


Fig. 9

DECLARATION AND POWER OF ATTORNEY FOR ORIGINAL U.S. PATENT APPLICATION

Attorney's Docket No. LAM2P322

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: ELECTROSTATIC CHUCK AND ITS MANUFACTURING METHOD, the specification of which,

(check one)

1. ☐ is attached hereto.
2. ☐ was filed on _____ as
U.S. Application Serial No. _____
and was amended on _____.
3. ☒ was filed on July 8, 1999 as
International PCT Application Serial No. PCT/JP99/03701
and was amended on _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, CFR § 1.56.

I hereby claim foreign priority benefits under Title 35, United States code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

_____ (Appl. No.)	_____ (Country)	_____ (Filing Date)
_____ (Appl. No.)	_____ (Country)	_____ (Filing Date)
_____ (Appl. No.)	_____ (Country)	_____ (Filing Date)

Priority Benefits Claimed?

☐ Yes ☐ No

☐ Yes ☐ No

☐ Yes ☐ No

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

_____ (Application Serial No.)	_____ (Filing Date)
_____ (Application Serial No.)	_____ (Filing Date)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Prior U.S. Application(s)

_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status - patented, pending, abandoned)
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_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status - patented, pending, abandoned)
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And I hereby appoint the law firm of Martine & Penilla, including **Peter B. Martine (Reg. No. 32,043); Albert S. Penilla (Reg. No. 39,487); Chester E. Martine, Jr. (Reg. No. 19,711); Edmund H. Mizumoto (Reg. No. 46,938); Joe A. Brock II (Reg. No. 46,021); Rick von Wohld (Reg. No. 48,018); Michael L. Gencarella (Reg. No. 44,703); George Brian Leavell (Reg. No. 45,436), and Jeffrey J. Brooks (Reg. No. 35,834) and Michael Brandt (Reg. No. 39,119), of Lam Research Corporation, as my principal attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:**

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Albert S. Penilla at telephone number (408) 749-6903

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

1-00
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